

ESP-12F

Pinbelegung

PIN	NAME	FUNCTION
1	RST	Reset the module
2	ADC	A/D conversion result input voltage range: 0 - 1 V Scope: 0 - 1024
3	EN	Chip Enable Pin (Active High)
4	GPIO16	GPIO16, activation from deep sleep mode
5	GPIO14	GPIO14, HSPI_CLK
6	GPIO12	GPIO12, HSPI_MISO
7	GPIO13	GPIO13, HSPI_MOSI, UART0_CTS
8	VCC	3.3 V
9	CS0	Chip selection
10	MISO	Slave-output, main-input
11	GPIO9	GPIO9
12	GPIO10	GPIO10
13	MOSI	Main-output, slave-input
14	SCLK	Clock
15	GND	GND
16	GPIO15	GPIO15, MTDO, HSPICS, UART0_RTS
17	GPIO2	GPIO2, UART1_TAXD
18	GPIO0	GPIO0
19	GPIO4	GPIO4